

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Hao Fang
Cameron C. Rabe
Case: 2-4
Serial No.: Unassigned
Filing Date: Concurrently Herewith
Group: Unassigned
Examiner: Unassigned

Title: Impedance-Matched Write Circuit with Shunted Matching Resistor

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying PTO Form 1449.

U.S. Patents

1. U.S. Patent No. 6,121,800
2. U.S. Patent No. 6,512,646

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,



Date: February 11, 2004

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**LIST OF PUBLICATIONS FOR
 APPLICANT'S INFORMATION
 DISCLOSURE STATEMENT**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
	6,121,800	09/19/00	Leighton et al.	327/110	
	6,512,646	01/28/03	Leighton et al.	360/46	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO	

OTHER DOCUMENTS

EXAMINER INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.